## Embedded Systems (MCQs)

1. Which of the following are the class of performance and functional based requirement embedded systems?

a) Stand-alone b) Real time

c) Mobile d) All the above

1. If the task deadline of an embedded system is completed within its deadline then it is called \_\_\_\_\_\_ type of embedded system.

a) Networked embedded system b) Soft real time

c) Hard real time d) Stand-alone

1. An embedded system contains how many number of bit processor?

a) 5 b) 4

c) 7 d) 2

1. Which of the following designed system factors are minimized for an embedded application?

a) Size b) Cost

c) Both a and b d) Performance

1. Which of the following is an example of medium scale embedded system?

a) DSP b) IP cameras

c) Both a and b d) Printer

1. Which of the following is an example of an embedded system that completes its task within its deadline?

a) Microwave oven b) Traffic Lights

c) Buzzer d) Both b and c

1. Which of the following are the examples of networked embedded systems?

a) ATM machines b) Smart watch

c) Micro-oven d) both a and b

1. Which design can be used to reduce the energy consumption of the embedded system?

a) Emulator b) Simulator

c) Debugger d) Compiler

1. Which design allows the reuse of the software and the hardware components?

a) Platform-based design b) Memory Design

c) Peripheral design d) Input design

1. Which of the following is approximated during hardware/software partitioning, during task-level concurrency management?

a) task-level concurrency management b) scheduling

c) High-level transformation d) compilation

1. Which type of memory is suitable for low volume production of embedded systems?

a) Non-volatile b) ROM

c) RAM d) Volatile

1. Which memory storage is widely used in PCs and Embedded Systems?

a) Flash memory b) SRAM

c) DRAM d) EEPROM

1. How much time period is necessary for the slave to receive the interrupt and transfer the data?

a) 16 T\_clk b) 4 T\_clk

c) 24 T\_clk d) 8 T\_clk

1. The special tale in the multitasking operating system is also known as

a) task access block b) task control block

c) task allocating block d) task address block

1. What is the purpose of address bus?

a) to provide data to and from the chip b) to select the read/write cycle

c) to select a location within the memory chip d) to select a specified chip

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